

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Steven Frank
Application No.:	10/735,610
Confirmation No.:	1959
Filed:	12/12/2003
Entitled:	GENERAL PURPOSE EMBEDDED PROCESSOR
Atty. Docket No.:	104853-0003

Group Art Unit: 2194

Examiner: Nathan E. Price

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Signed: /Benjamin E. Berman/
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INTERVIEW SUMMARY

Dear Sir:

This is filed in connection with the Examiner Interview conducted on August 14, 2008, addressing the allowability of the claims as presented in the Response to Non-Final Office Action, filed June 5, 2008.

The Interview addressed the pending claims in view of Brown (U.S. Patent No. 6,240,508), Jagannathan (U.S. Patent No. 5,692,193) and Sekiguchi (US 2001/0016879).

More specifically, discussed at interview were features of the claimed invention (e.g., claim 1) and their absence from the prior art. For example, as discussed, none of the cited references teach, individually or in combination, an event delivery mechanism that is in communication with a plurality of processing units and that delivers events to respective threads without execution of instructions by the processing units, e.g., as required by claim 1. As noted by Applicants the cited references are deficient in these and other regards.

At the request of the Examiner, the Applicants pointed out examples of disclosure in Applicants' specification of mechanisms for thread processing and event delivery. As noted at interview, those examples are merely that ... examples.

From this discussion, the Applicants understood that the Examiner was unable to identify disclosure in the cited references tending to, individually or in combination, teach or suggest the claimed invention. The Applicants understand the Examiner may conduct a further search and issue a notice of allowance or a new non-final rejection.

Respectfully submitted,

Date: August 14, 2008

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